REMARKS

Claims 1-16 and 31 have been amended by way of this preliminary amendment. Accordingly, claims 1-73 are now pending.

In accordance with 37 C.F.R. § 1.121(c)(1)(ii), separate sheet(s) with the rewritten claims marked-up to show the changes made to the previous version of the claims, is submitted herewith as Appendix A.

Prompt consideration of the pending claims is respectfully requested.

Respectfully submitted,

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APPENDIX A
SEPARATE SHEETS WITH MARKED-UP VERSION OF CLAIMS PER 37
C.F.R § 1.121(c)(1)(ii)

Claim 1 has been amended as follows:

1   1. (Amended)   A communication device for use in a

2   communications system [for generating an OFDM signal having

3   frequency] that uses multiple tones distributed over a

4   predetermined bandwidth [, the communication system] to

5   communicate data, the device comprising:

6   [an allocation circuit that defines an allocated

7   tone set selected from frequency tones distributed over a

8   predetermined bandwidth;]

9   a mapping circuit that receives data symbols

10 [from a symbol constellation] and maps the symbols to

11 prescribed time instants in a [time domain symbol duration]

12 predetermined time interval to generate a discrete signal

13 [of] including mapped symbols, each mapped symbol

14 corresponding to a discrete point in time; and

15   an interpolation circuit that receives the

16 discrete signal and generates a continuous signal by

17 applying [predetermined] an interpolation [functions]

18 function to the discrete signal, the interpolation

19 [functions] function operating on the discrete signal such

20 that a frequency response of the continuous signal includes

21 sinusoids having non-zero values at a first set of tones,

22 the first set of tones being a subset of said multiple

23 tones, the non-zero value at each of said first set of

24 tones being a function of a plurality of mapped symbols

25 corresponding to different discrete points in time, the

26 frequency response of the continuous signal also including

1

27   zero values at a second set of tones, the second set of

28   tones being different from said first set of tones and

29   being another subset of said multiple tones [values of the

30   continuous signal at the prescribed time instants are equal

31   to the mapped symbols and a frequency response of the

32   continuous signal includes sinusoids having non-zero values

33   at frequency tones within the allocated tone set and zero

34   values at the remaining frequency tones ; and

35   a sampling circuit that samples the continuous

36   signal at discrete time instants distributed over the time

37   domain symbol duration to generate a digital signal sample

38   vector].

          Claim 2 has been amended as follows:

1   2. (Amended)   The [communication system] device of

2   claim 1 wherein the discrete time instants are defined

3   within the range of 0, T/N, 2T/N, …, T(N-1)/N, where N is a

4   total number of time instants in the [time domain symbol

5   duration] predetermined time interval.

          Claim 3 has been amended as follows:

1   3. (Amended)   The [communication system] device of

2   claim 1 wherein the frequency tones within the allocated

3   tone set are contiguous frequency tones, and the prescribed

4   time instants are equally spaced and uniformly distributed

5   over one symbol duration.

          Claim 4 has been amended as follows:

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2

1   4. (Amended) The [communication system] <u>device</u> of
2 claim 1 wherein the frequency tones within the allocated
3 tone set are equally spaced frequency tones, and the
4 prescribed time instants are equally spaced and uniformly
5 distributed over a fraction of one symbol duration.

Claim 5 has been amended as follows:
1

1   5. (Amended) The [communication system] <u>device</u> of
2 claim 4 wherein a fraction of one symbol duration is
3 defined by 1/L where L is the spacing between two adjacent
4 allocated frequency tones in the allocated tone set.

Claim 6 has been amended as follows:
1

1   6. (Amended) The [communication system] <u>device</u> of
2 claim 1 wherein a total number of discrete time instants is
3 greater than or equal to a total number of frequency tones
4 distributed over the predetermined bandwidth.

Claim 7 has been amended as follows:
1

1   7. (Amended) The [communication system] <u>device</u> of
2 claim 1 wherein the interpolation circuit further includes
3 a memory for storing the predetermined interpolation
4 functions, and an interpolation function module for
5 retrieving the interpolation functions from the memory and
6 applying the interpolation functions to the discrete signal
7 to generate the continuous signal.

3

Claim 8 has been amended as follows:

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1   8. (Amended) The [communication system] <u>device</u> of
2   claim 7 wherein the interpolation functions comprise a
3   matrix of precomputed sinusoidal waveforms.

Claim 9 has been amended as follows:

1

1   9.   (Amended) The [communication system] <u>device</u> of
2   claim 7 wherein the interpolation functions comprise
3   continuous interpolation functions.

Claim 10 has been amended as follows:

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1   10. (Amended) The [communication system] <u>device</u> of
2   claim 1 wherein the mapping circuit replicates the discrete
3   signal of mapped symbols to generate an infinite series of
4   mapped symbols over prescribed time instants covering a
5   time interval from $-\infty$ to $+\infty$.

Claim 11 has been amended as follows:

1

1   11.   (Amended)   The [communication system] <u>device</u> of
2   claim 10 wherein the interpolation functions comprise sinc
3   interpolation functions, and the interpolation circuit
4   applies the sinc interpolation functions to the infinite
5   series of mapped symbols.

4

Claim 12 has been amended as follows:

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1   12. (Amended) The [communication system] <u>device</u> of
2   claim 1 wherein the data symbols are complex symbols
3   associated with a symbol constellation.

Claim 13 has been amended as follows:

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1   13. (Amended) The [communication system] <u>device</u> of
2   claim 1 further including a digital signal processor for
3   implementing the mapping circuit and the interpolation
4   circuit.

Claim 14 has been amended as follows:

1

1   14. (Amended) The [communication system] <u>device</u> of
2   claim 1 further including a cyclic prefix circuit for
3   receiving the digital signal sample vector from the
4   sampling circuit and prepending a cyclic prefix to the
5   digital signal sample vector.

Claim 15 has been amended as follows:

1

1   15. (Amended) The [communication system] <u>device</u> of
2   claim 14 wherein the cyclic prefix circuit operates to copy
3   an end portion of the digital signal sample vector and
4   prepend the end portion to a beginning portion of the
5   digital signal sample vector.

5

Claim 16 has been amended as follows:

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1   16. (Amended) The [communication system] <u>device</u> of

2   claim 1 further including a digital to analog converter

3   operable to receive the digital signal sample vector and

4   generate an analog signal for transmission within the

5   communication system.

Claim 31 has been amended as follows:

1

1   31. (Amended) The communication system of claim [30]

2   <u>1</u> wherein the continuous signal comprises an OFDM

3   communication signal <u>and wherein the value of the</u>

4   <u>continuous signal at each of the prescribed time instants</u>

5   <u>is a function of the mapped symbol at said prescribed time</u>

6   <u>instant</u>.

6